

## Summary of my research and teaching activities

<b>Personal data</b>	<p><b>Guy GOGNIAT</b>  Born July 17th, 1970 in Palaiseau (91)  French nationality  43 years old, married, 2 children</p>
<b>Laboratory</b>	<p>Laboratory Lab-STICC  CNRS UMR 6285, University of Bretagne-Sud – UEB</p>
<b>Curriculum Vitae</b>	<p><b>Current position</b></p> <ul style="list-style-type: none"> <li>• <b>Professor at University of Bretagne-Sud</b>  <i>Teaching activities at University of Bretagne-Sud, Department of Electrical and Computer Engineering  Research at Laboratory des Sciences et Techniques de l'Information, de la Communication et de la Connaissance (Lab-STICC)</i></li> </ul> <p><b>Education</b></p> <ul style="list-style-type: none"> <li>• <b>Research habilitation</b> University of Bretagne-Sud  <i>Graduated - October 26th, 2007</i></li> <li>• <b>D.Sc. in Electrical Engineering</b> University of Nice – Sophia Antipolis  <i>Outstanding thesis award, graduated - November 27th, 1997</i></li> <li>• <b>M.Sc. in Electrical Engineering</b> University of Paris Sud Orsay  <i>Graduated - September 1994</i></li> <li>• <b>Diploma of engineer in Electrical Engineering</b> University of Paris Sud Orsay  <i>Graduated - September 1994</i></li> </ul> <p><b>Professional experiences</b></p> <ul style="list-style-type: none"> <li>• September 2009/Today <b>Professor at University of Bretagne-Sud</b></li> <li>• September 2005/August 2009 <b>Associate Professor at University of Bretagne-Sud</b></li> <li>• November 2004/August 2005 <b>Invited Researcher (CRCT – Grant ERE DGA)</b>  <i>Department of Electrical and Computer Engineering,  University of Massachusetts, Amherst, MA, USA</i></li> <li>• September 1998/October 2004 <b>Associate Professor at University of Bretagne Sud</b></li> <li>• September 1997/ August 1998 <b>Assistant Professor University of Nice – Sophia Antipolis</b></li> <li>• September 1994/ August 1997 <b>Teaching Assistant University of Nice – Sophia Antipolis</b></li> <li>• September 1994/ August 1997 <b>PhD Student (Grant MENRT)</b>  <i>Laboratory I3S, CNRS – University of Nice – Sophia Antipolis</i></li> </ul>
<b>Teaching keywords</b>	<p>Digital and analog design, ASIC design, FPGA/CPLD, VHDL, C, cryptography, processors, microcontroller, codesign, adaptive systems, control systems, programmable automata, industrial and data communication networks, data acquisition, physics of semi-conductors, specification languages for embedded systems (Esterel, SystemC, VHDL-AMS...)</p>
<b>Teaching activities</b>	<p><b>L2 level – Field of electrical and computer engineering</b>  <b>L3 level – Field of electrical and computer engineering</b>  <b>M1 level – Field of electrical and computer engineering</b>  <b>M2 level – Field of electrical and computer engineering and cryptography</b></p>
<b>Publications related to teaching activities</b>	<p>2 publications en international conferences</p>
<b>Research keywords</b>	<p>Codesign, partial and dynamic reconfigurable architectures, self partial reconfiguration of FPGAs, security of embedded systems, functional partitioning, optimization, synthesis design flow, architecture of embedded systems, MPSoC, hardware/software integration, system level estimation, hardware and software, design space exploration, partitioning, RTOS, telecommunication applications, multimedia and cryptography, Model driven design methodologies (MDA)</p>
<b>Research awards</b>	<p>PEDR Outstanding research award (PEDR 2003/2007)  PEDR Outstanding research award (PEDR 2007/2011)  PES Outstanding research award (PES 2013/2016)</p>
<b>Advising</b>	<p>18 Ph.D. students (16 graduated)  3 Post-Doc  17 M.S. students</p>

<b>Ph. D. Thesis committee</b>	64 Ph. D. Thesis committee: - 3 as a Member (University of Nice – Sophia Antipolis, University of Bretagne-Sud...) - 13 as an Adviser (University of Bretagne-Sud) - 40 as a Reviewer (University Montpellier II, University of Rennes I, Institut National des Sciences Appliquées de Rennes, Institut Polytechnique de Grenoble, University of Sciences and technology of Lille, University of cergy Pontoise, University of Pierre et Marie Curie...) - 10 as a Chair (Télécom Bretagne, Université de Bretagne Occidentale, Université de Rennes I...)
<b>HDR Thesis committee</b>	7 HDR Thesis committee: - 3 as a Member (University of Rennes I, University of Bordeaux, University of Pierre et Marie Curie) - 4 as a Reviewer (University of Bordeaux, Telecom Bretagne, University of Nice – Sophia Antipolis, University of Tunis)
<b>Professional service</b>	<p><b>Steering committee of international workshops</b>  <i>CryptArchi 2008, DASIP (2009, 2010, 2011, 2012, 2013)</i></p> <p><b>Program chair of international conferences</b>  <i>DASIP 2007, ReCoSoC 2012</i></p> <p><b>Track Chair of international conferences</b>  <i>FPL 2013</i></p> <p><b>Organization committee member of international conferences</b>  <i>DASIP (2008,2009, 2010, 2011, 2012, 2013), CryptArchi 2008</i></p> <p><b>Organization of special session</b>  <i>DASIP 2009, ReCoSoC 2011, SoC 2013</i></p> <p><b>Program committee member of international conferences</b>  <i>ERSA (2008, 2009, 2010, 2011, 2012), DASIP (2008, 2009, 2010, 2011, 2012, 2013), CryptArchi (2008, 2009, 2010, 2011, 2012, 2013), ReConFig (2008, 2009, 2010, 2011, 2012, 2013), IEEE FCCM (2009, 2010, 2011, 2012, 2013), ARC (2009, 2010, 2011, 2012, 2013), WESS (2010, 2011, 2012), RAW (2011, 2012, 2013), CHANGE (2011, 2012), IEEE ICFPT (2011, 2012, 2013), IEEE ISVLSI (2008, 2013), FPL (2013), PATMOS (2013), ReCoSoC (2012, 2013), ICESTI(2012), ICECS (2011),IEEE IDT (2008, 2009), C&amp;ESAR 2008, HEART (2013), SoC (2013)</i></p> <p><b>International workshop publicity co-chair</b>  <i>ViPES (2013, 2014)</i></p> <p><b>Review committee member of international conferences</b>  <i>GRETSI (2009,2013), IEEE WCNC 2008, IEEE ICC 2009, CHES 2009, IEEE NEWCAS (2010, 2011), CHES 2010, SOFTCOM 2011, SOCC (2012, 2013), GLSVLSI 2013</i></p> <p><b>Best paper committee of international conferences</b>  <i>ICFPT (2011), FPL (2013)</i></p> <p><b>Moderator of international conferences</b>  <i>FPL (2013), DASIP (2008, 2009, 2010), ReCoSoC (2011, 2012), CryptArchi (2008, 2009, 2010, 2012), IEEE ISVLSI 2008</i></p> <p><b>Invitation Keynote Speaker</b>  <i>VARY 2010, SBCCI 2011, PHYSIC 2011</i></p> <p><b>Round tables of international conferences</b>  <i>Summer school MDD4DRES 2006, ReCoSoC'07, IEEE ICFPT'08, ERSA 2010</i></p> <p><b>Summer school</b>  <i>MDD4DRES 2006, ARCH109, FETCH2012</i></p> <p><b>Guest Editor</b>  <i>EURASIP Journal on Embedded Systems, Special issue – Design and Architectures for Signal Image Processing 2009</i>  <i>Book Algorithm-Architecture Matching for Signal and Image Processing Springer 2010</i>  <i>Book Securiy trends for FPGAs: From Secured to Secure Reconfigurable Systems Springer 2011</i>  <i>ACM Transactions on Reconfigurable Technology and Systems (TRETS), Special Section 2013</i></p> <p><b>Review committee member of international journals</b>  <i>Revue scientifique francophone Traitement du signal (TS), Journal IEE Proceedings – Computers and Digital Techniques, IEEE Transactions on VLSI, IEEE Transactions on CAD, IEEE Transactions on TCSVT, IEEE Design &amp; Test of Computers, ACM Transactions on TRETS, EURASIP Journal on Embedded Systems, International Journal on Computers and Electric Engineering, International Journal of Reconfigurable Computing, The Journal of VLSI Signal Processing, Springer Journal of Real-Time Image Processing, Elsevier, Integration, the VLSI Journal, Elsevier Journal on Microprocessors and Microsystems</i></p> <p><b>National and international review committee member for research programs</b>  <i>Expert international NWO Computer Science Open Competition 2005 (Hollande)</i>  <i>Expert international Swiss National Science Foundation (SNSF) 2009/2010/2011 (Suisse)</i>  <i>Expert international PHC Germaine de Staël 2011(Suisse)</i>  <i>Expert PSOC INS2I 2011</i>  <i>Agence Nationale de la Recherche – Appel Architectures du futur (2006, 2007)</i>  <i>Agence Nationale de la Recherche – Appel Systèmes Embarqués et Grandes Infrastructures - ARPEGE (2008, 2009)</i>  <i>Agence Nationale de la Recherche - Appel Retour Post-Doctorants (2010)</i>  <i>Agence Nationale de la Recherche - Appel EMERGENCE (2010)</i></p>

	<p><i>Agence Nationale de la Recherche - Programme Blanc International (2012)</i></p> <p><i>Agence Nationale de la Recherche - Programme INS (2012)</i></p> <p><i>Agence Nationale de la Recherche - Programme JCJC (2013)</i></p>
<b>International collaborations</b>	<p>8 international collaborations with Master students, PhD students and Professor exchanges:</p> <p>University of Massachusetts, Amherst, USA, George Mason University, Fairfax, USA, Cambridge University, UK, Aalborg University, Denmark, Ecole Nationale d'Ingénieurs de Sfax, Sfax, Tunisia, University of de Sao Paolo, Brazil, University of Edinburgh, UK, Institut Alari, Switzerland, Ruhr-Universität Bochum, Germany</p>
<b>Funding</b>	<p><b>TSUNAMY</b> 2013/2017, ANR Project, LIP6, CEA LIST, LabHC, Lab-STICC</p> <p><b>FAMOUS 2013</b> 2009/2013, ANR Project, <i>Inria Lille, Lab-STICC, Inria Rhône Alpes, LE2I, Sodius</i></p> <p><b>SecReSoC 2013</b> 2009/2013, ANR Project, <i>LIRMM, Telecom ParisTech, LHC, Lab-STICC, Netheos</i></p> <p><b>MOPCOM 2010</b> 2007/2010, RNTL Project, <i>Thales, Thomson, ENSIETA, SUPELEC, IRISA, LESTER, Sodius</i></p> <p><b>AETHER 2009</b> 2006/2009, European Project IST-FET (4th call ACA / FP6)</p> <p><b>ICTeR 2009</b> 2006/2009, ANR Project, <i>LIRMM, ENST, CEA LIST, LESTER, Netheos</i></p> <p><b>SecureNIOS 2007</b> 2006/2007, <i>LESTER, VSPG (UMASS, USA)</i></p> <p><b>PROSYR2006</b> 2003/2006, CMCU Project, <i>LESTER, ENIS (Sfax, Tunisie)</i></p> <p><b>SANES 2005</b> 2004/2005, DGA ERE, <i>LESTER, VSPG (UMASS, USA)</i></p> <p><b>SecureFPGA 2004</b> 2003/2004, <i>LESTER, VSPG (UMASS, USA)</i></p> <p><b>DARSoC 2003</b> 2002/2003, <i>LESTER, VSPG (UMASS, USA)</i></p> <p><b>A3S 2005</b> 2003/2005, RNRT Project, <i>Thales Communications, Softeam, Mitsubishi, LESTER</i></p> <p><b>POMARD 2004</b> 2003/2004, CNRS Project, <i>R2d2, LIEN, LIRMM, LE2I, ETIS, CEA LIST, A&amp;S, LESTER</i></p> <p><b>EPICURE 2003</b> 2001/2003, RNTL Project, <i>I3S, LESTER, CEA LIST, Thales Com., Esterel-Technologies</i></p> <p><b>MACGTT 2002</b> 2000/2002, CNRS Project, <i>I3S, LASTI, LESTER</i></p> <p><b>D2ASR 1999</b> 1999, Industrial Project, <i>LESTER, SERPE-lesm</i></p> <p><b>CODEF 2001</b> 1998/2001, Industrial Project, <i>I3S, Philips/VLSI Technology</i></p>
<b>Publications</b>	<p>17 international journals (<i>ACM TODAES, ACM TECS, IEEE TCAD, IEEE TVLSI, EURASIP, ELSEVIER</i>)</p> <p>2 national journal</p> <p>4 book chapters</p> <p>2 books (Springer)</p> <p>103 publications in international conferences</p> <p>14 publications in national conferences</p> <p>1 patent</p>