# Bubble check: simplified check node architecture for nonbinary LDPC

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The Extended Min Sum (EMS) algorithm has recently been proposed for non-binary LDPC decoding. In this letter, we present a simplified version of the EMS check node update, named bubble check. This novel technique can reduce the number of comparison operations by a factor of 3, resulting in a lower hardware complexity, without introducing any significant perfomance degradation.

*Introduction*: Non-binary Low-Density Parity-Check (NB-LDPC) codes are constructed as a set of parity equations over a Galois Field GF(q). They are known to be an efficient alternative to binary LDPC for the transmission of short frames. However, high decoding complexity of NB-LDPC codes (especially the check node processors) remains a bottleneck in their hardware realization. In this letter we propose a low complexity algorithm named Bubble Check to perform efficiently the check node processing.

*Check node processing in NB-LDPC decoders*: Using the forward-backward algorithm, a check node of degree  $d_c$  can be decomposed in  $3(d_c-2)$  Elementary Check Nodes (ECN) where an ECN has two input messages **U** and **V** and one output message **E**. Each message **U** contains the probability distribution of the associated variable u, i.e. the probability density function **U** defined by the q probabilities  $\{P(u=\alpha)\}_{\alpha\in GF(q)}$ . In order to verify the elementary check equation, u, v and e must verify the parity equation: u+v+e=0, where "+" stands for the addition in GF(q). Assuming that **U** and **V** are known, the extrinsic information **E** can be

computed for every value  $\gamma$  of GF(q) as [1]:

$$P(e=\gamma) = \sum_{(\alpha,\beta)\in GF(q)^2/\alpha+\beta=\gamma} P(e=\alpha) \times P(v=\beta)$$
(1)

The direct computation of (1) requires  $q^2$  multiplications and additions. Performing the computation in the frequency domain reduces the computational complexity to  $q.\log(q)$  multiplications and additions [2]. The complexity of (1) can be further reduced by *a*) only considering the  $n_m$  highest values of vectors **U**, **V** and **E**; *b*) performing the operation using the Log-Likelihood Ratio (LLR)  $LLR_U(\alpha)=Log(P(u=\alpha)/P(u=\alpha_0))$  where  $\alpha_0$  is the GF(*q*) value with the highest probability and *c*) approximating the addition in the probability domain by the maximum in the LLR domain. These are the simplifications considered in the EMS algorithm [3]. Equation (1) then becomes:

$$LLR_{E}(\gamma) = \frac{MAX}{\alpha \in \mathbf{U}^{gf}(n_{m}), \beta \in \mathbf{V}^{gf}(n_{m})/\alpha + \beta = \gamma} LLR_{U}(\alpha) + LLR_{V}(\beta)$$
(2)

where  $\mathbf{U}^{\text{gf}}(n_m)$  (respectively  $\mathbf{V}^{\text{gf}}(n_m)$ ) is the set of the  $n_m$  GF(q) values corresponding to the highest LLR values of **U** (respectively **V**). Since the LLR are all negative, it is more convenient to consider LLR<sub>m</sub> = -LLR to deals with positive values. Note that using LLR<sub>m</sub> instead of LLR replaces the MAX operator in equation (2) by the MIN operator. In the rest of the paper, **U**(*i*) will denote the *i*<sup>th</sup> smallest LLR<sub>m</sub> value and  $\mathbf{U}^{\text{gf}}(i)$  the associated GF(q) symbol (*i.e.*  $\mathbf{U}(i) = \log(P(u=\mathbf{U}^{\text{gf}}(0)=\alpha_0)-\log(P(u=\mathbf{U}^{\text{gf}}(i)))$ . Thus, the values  $\mathbf{U}(i)_{i=1..nm}$  verify  $\mathbf{U}(1)=0 \le \mathbf{U}(2) \le ... \le \mathbf{U}(n_m)$ . The same convention holds also for **V** and **E**.

From **U** and **V**, it is possible to generate the  $n_m \times n_m$  matrix  $\mathbf{T}_{\boldsymbol{\Sigma}}$  defined as  $\mathbf{T}_{\boldsymbol{\Sigma}}(i, j) = \mathbf{U}(i) + \mathbf{V}(j)$ , for  $(i,j) \in [1, n_m]^2$ . As **U** and **V** are sorted in increasing order,  $\mathbf{T}_{\boldsymbol{\Sigma}}$  satisfies:

**Theorem 1:** 
$$\forall$$
 (*i*,*j*)  $\in$  [1,  $n_m$ ]<sup>2</sup>,  $\forall$  (*i*',*j*')  $\in$  [1,  $n_m$ ]<sup>2</sup>,  $i \leq i'$  and  $j \leq j' \Rightarrow T_{\Sigma}(i, j) \leq T_{\Sigma}(i',j')$ 

In other words, the lines and the columns of  $T_{\Sigma}$  are still sorted in increasing order. Using this

property, the EMS tries to extract the  $n_m$  lowest values of  $T_{\Sigma}$  corresponding to  $n_m$  distinct GF(*q*) symbols as described by the following.

#### The EMS algorithm

- 1. Initialization: **E** =  $\emptyset$  and initialize the set of  $n_m$  competing candidates, or competing bubbles (CB), with the first column **T**<sub> $\Sigma$ </sub>(*i*,1) of **T**<sub> $\Sigma$ </sub>.
- 2. FOR k=1 to nop LOOP
  - a. Extract the lowest value of CB  $\mathbf{T}_{\mathbf{\Sigma}}(i,j)$  and compute the associated GF(*q*) value  $\gamma(i,j) = \mathbf{U}^{\text{gf}}(i) + \mathbf{V}^{\text{gf}}(j)$ .
  - b. Replace the extracted value in CB by  $T_{\Sigma}(i,j+1)$ .
  - c. IF  $\gamma(i,j) \notin \mathbf{E}^{gf}$ , then add the new candidate to the set **E**, EXIT loop if card(**E**)= $n_m$ .

The value  $n_{op}$  is the number of allowed trials to find  $n_m$  distinct GF(*q*) values in **E**, thus  $n_{op} \ge n_m$ . If  $n_{op}$  is large enough, then equation (2) will be exactly computed. In order to extract the lowest value of CB in one clock cycle, [3] proposes to perform a parallel insertion of the new incoming value in CB. This operation requires  $n_m$  comparisons in parallel. Since  $n_{op}$  cycles are needed, the global complexity of the EMS algorithm is then  $n_{op} \times n_m$  comparisons.

*The bubble check principle:* As  $T_{\Sigma}(1,1)$  is the minimum value of  $T_{\Sigma}$ ,  $E(1) = T_{\Sigma}(1,1)$  can be extracted directly. Similarly, the second lowest value of  $T_{\Sigma}$  is either  $T_{\Sigma}(2,1)$  or  $T_{\Sigma}(1,2)$ , i.e. there are only two possible candidates for E(2). The third element E(3) can be either between  $T_{\Sigma}(3,1)$  and  $T_{\Sigma}(1,2)$  or between  $T_{\Sigma}(1,3)$  and  $T_{\Sigma}(2,1)$  according to the selected second smallest value i.e.  $T_{\Sigma}(2,1)$  or  $T_{\Sigma}(1,2)$  respectively. A more general question is: what is the maximum number  $n_b$  of competing bubbles that needs to be checked in order to extract the  $n^{th}$  lowest value? This problem can be answered by reversing the question: assuming  $n_b$  bubbles, what

is the maximum number of lowest values that can be extracted from  $T_{\Sigma}$ ? When the  $n_b$  bubbles form a diagonal { $T_{\Sigma}$  (k,  $n_b$ -k+1)} $_{k=1..n_b}$  in  $T_{\Sigma}$ , if each of the  $n_b(n_b-1)/2$  elements above the diagonal are smaller or equal to the  $n_b$  elements of the diagonal, then the  $n_b(n_b-1)/2$ +1 lowest value is one of the  $n_b$  bubbles. In other words, with  $n_b$  bubbles, it is possible to extract at least  $n = n_b(n_b - 1)/2$ +1 values. Reciprocally, by inverting this relation, the minimum value  $n_b$ required to extract the  $n^{th}$  value will be given by  $n_b = \Psi(n) = \text{ceil}((\sqrt{8n+1}-1)/2)$  where ceil(x) represents the smallest integer greater than or equal to x. Consider the example in Fig. 1, where a grey circle represents a candidate that has already been selected to be an element of E and a white circle represents a competing bubble for the  $n^{th}$  position. In Fig. 1(a), 3 candidates compete for the 5<sup>th</sup> position in E, Fig. 1(b) and 1(c) represent the worst case, or the maximum number of candidates for the  $n^{th}$  position for n=7 and 11, respectively. Note that the number of candidates is exactly  $\Psi(n)$  when the  $(n-1)^{th}$  first elements in E form a triangle in  $T_{\Sigma}$ .

Theoretical complexity reduction: As in [3], the core component of the algorithm is the sorter that delivers the lowest value in CB to the output message **E** and replaces it with a new element of  $T_{\Sigma}$ . A corollary of **Theorem 1** is that the maximum theoretical size of the sorter that feeds **E** is  $\Psi(n_m)$ . This means that the complexity of the elementary check is no longer dominated by  $n_{op} \times n_m$  but by  $n_{op} \times \Psi(n_m)$ , which results in a significant complexity reduction (for example, for  $n_m$ =15, the number of comparison operations is reduced by a factor of 3).

*The bubble check algorithm:* The main originality of the bubble check algorithm is its twodimensional (2D) strategy for the values in  $T_{\Sigma}$  that successively feed the sorter. This means that once the element  $T_{\Sigma}(i, j)$  is moved from the sorter to the vector **E**, it can be replaced by either  $T_{\Sigma}(i+1, j)$  or  $T_{\Sigma}(i, j+1)$ . To control this, we introduce a flag "horizontal" (*H*): if *H* is true, then the value  $T_{\Sigma}(i, j)$  is replaced by  $T_{\Sigma}(i, j+1)$ ; If *H* is not true (i.e. Vertical mode), then  $T_{\Sigma}(i, j)$ is replaced by  $T_{\Sigma}(i+1, j)$ . The step 2.c) of the EMS algorithm, i.e. the rule to replace the extracted value  $T_{\Sigma}(i, j)$  in the set CB is then modified as follows:

#### Modified step 2.b)

Flag control: change the value of *H*.

a) when (i=1) then H=1;

b) when  $(j=1 \text{ and } i \ge n_b)$  then H=0.

Test if  $T_{\Sigma}(i+\overline{H}, j+H)$  has already been introduced in the set CB.

If no, then include  $T_{\Sigma}(i+\overline{H}, j+H)$  in CB;

If yes, then include  $T_{\Sigma}(i+H, j+\overline{H})$  in CB.

Fig 2. shows the extraction of the 9 smallest values in  $T_{\Sigma}$  using the bubble check algorithm with  $n_b$ =4 for inputs **U**={0,3,5,6,8,...} and **V**={0,2,6,9,10,...}. In this example, the flag changes from horizontal (*H*=1) to vertical (*H*=0) at the 7<sup>th</sup> clock cycle.

Simulation results: We simulated the bubble check with ultra-sparse NB-LDPC codes designed in GF(q=64) and characterized by a fixed variable node degree  $d_v$ =2 [4]. Codewords of length *N*=48, 96, 192, 288, 384 symbols (i.e. a length of *N*x6 bits) and rates *R*=1/2, 2/3, 3/4 and 5/6 were considered. The decoder performs an EMS with horizontal shuffle scheduling and forward/backward processing, with  $n_m$ =16,  $n_{op}$ =18 and offset=1 or 2 (see [3] for the definition of the offset value). Fig. 3 shows simulation results for *N*=192 and *R*=1/2, with different number of bubbles in the sorter ( $n_b$  = 2, 3, 4, 5 and  $\Psi(n_m)$ =6). As shown in the figure,

there is no performance loss with the bubble check algorithm for  $n_b \ge 4$ . For  $n_b = 3$  and 2, the performance loss is around 0.04 dB and 0.4 dB respectively. The simulation of other code lengths and rates (not shown in this letter) confirms that the performance of the bubble check algorithm with only  $n_b=4$  bubbles remains identical to the performance of the EMS algorithm. This shows that it is possible to further reduce the complexity of the algorithm by using  $n_b < \Psi(n_m)$ .

*Conclusion:* In this letter we have presented the bubble check, a novel algorithm for the elementary check node processing of NB-LDPC decoders that introduces significant complexity reduction without performance loss. The check node update being the bottleneck of the decoder complexity, we believe that this complexity reduction is a key feature for possible future hardware implementation (FPGA or ASIC) of NB-LDPC decoders.

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### Figure captions:

Fig. 1 Candidate values in  $T_{\Sigma}$  to occupy the  $n^{th}$  position of **E**. A grey circle stands for a value already selected from  $T_{\Sigma}$  and a white circle stands for a candidate value (a bubble).

- (a) Input vectors **U** and **V**, matrix  $T_{\Sigma}$  and 3 bubbles for n = 5
- (b) Maximum number of candidates for n = 7. The first six values in **E** form a triangle in  $T_{\Sigma}$
- (c) Maximum number of candidates for n = 11. The first ten values in **E** form a triangle in  $T_{\Sigma}$

Fig. 2 Extraction of the 9 smallest values in  $T_{\Sigma}$  using the bubble check algorithm with  $n_b$ =4 for inputs **U**={0,3,5,6,8,...} and **V**={0,2,6,9,10,...}.

- (a) Initial configuration. Extraction of the first value  $E(1)=T_{\Sigma}(1,1)=0$ , flag H=1 (horizontal mode).
- (b) Extraction of the 7<sup>th</sup> smallest value in  $T_{\Sigma}$ . Since *i*=*n*<sub>b</sub>, the flag *H* is flipped to 0 (vertical mode): the next bubble will then be  $T_{\Sigma}(5,1)$ .
- (c) Extraction of the 9<sup>th</sup> smallest value of  $T_{\Sigma}$ , *H* is still equal to 0.

Fig. 3 Simulation results for N=192, R=1/2,  $n_m = 16$ ,  $n_{op} = 18$ , offset = 1.0 and at 20 decoding iterations. "EMS" corresponds to the EMS algorithm defined in [3].

## Figure 1







c)





0	2	6	9	10	_			
0	2	6	0	10				
3	5	9	12	13				
5	$\bigcirc$	11	14	15				
6	8	12	15	16				
8	10	14	17	18				
<i>H</i> =0 b)								

	0	2	6	9	10				
0	0	2	6	0	10				
3	3	5	0	12	13				
5	6	7	11	14	15				
6	6	8	12	15	16				
8	8	10	14	17	18				
H=0									
	c)								



